

| PERIODS→         | 1   | 2                              | 3  | 4                           | 12:40<br>Noon<br>TO<br>1:30<br>P.M. | 5   | 6                            | 7                               |  |
|------------------|---|--------------------------------|--|-----------------------------|-------------------------------------|---|------------------------------|---------------------------------|--|
| TIMINGS→         | 9:20 A.M.<br>TO<br>10:10 A.M.               | 10:10 A.M.<br>TO<br>11:00 A.M. | 11:00 A.M. TO<br>11:50 A.M.                    | 11:50 A.M. TO<br>12:40 Noon |                                     | 1:30 P.M<br>TO<br>2:20 P.M.                             | 2:20 P.M.<br>TO<br>3:10 P.M. | 3:10 P.M.<br>TO<br>4:00 P.M.    |  |
| ↓DAYS↓           | ↓ SUBJECTS ↓                                |                                |  |                             | LUNCH                               | ↓ SUBJECTS ↓  |                              |                                 |  |
| <b>MONDAY</b>    | ←Wireless Mobile<br>Communication System →  |                                | Data and Computer<br>Communication<br>Networks | *****                       |                                     | Advanced Digital Design with Verilog HDL                |                              | Neural Network &<br>Fuzzy Logic |  |
| <b>TUESDAY</b>   | Neural Network & Fuzzy Logic                |                                | Coding Theory and<br>Techniques                | *****                       |                                     | <b>Lab-II In the Computer Lab-Room no-5716</b>          |                              |                                 |  |
| <b>WEDNESDAY</b> | Data and Computer Communication<br>Networks |                                | Computer Vision                                | *****                       |                                     | <b>Seminar-II In the P.G. Seminar Hall room No-5711</b> |                              |                                 |  |
| <b>THURSDAY</b>  | Coding Theory and Techniques                |                                | System on Chip<br>Architecture                 | *****                       |                                     | *****   | *****                        | *****                           |  |
| <b>FRIDAY</b>    | System on Chip Architecture                 |                                | Advanced Digital Design<br>with Verilog HDL    | *****                       |                                     | *****   | *****                        | *****                           |  |
| <b>SATURDAY</b>  | *****                                       |                                | *****  | *****                       |                                     | *****   | *****                        | *****                           |  |

**↓ ALLOCATION OF SUBJECTS ↓**

| S.NO. | NAME OF THE SUBJECT                      | SYLLABUS<br>REFERANCE NO- | SUBJECT<br>CODE | CORE /<br>ELECTIVE | NAME OF THE FACULTY                  | DESIGNATION               |
|-------|--|---------------------------|-----------------|--------------------|--------------------------------------|---------------------------|
| 1     | Data and Computer Communication Networks | EC-504                    | DCCN            | CORE               | Mrs. Ayesha Naaz                     | Asso.Prof.                |
| 2     | Coding Theory and Techniques             | EC-594                    | CTT             | CORE               | Mr. Iftekharuddin                    | Asso. Prof.               |
| 3     | System on Chip Architecture              | EC-511                    | SOCA            | ELECTIVE           | Mr. J.K. Nag                         | Asso. Prof.               |
| 4     | Computer Vision                          | EC-514                    | CV              | ELECTIVE           | Dr. C. Rangaiah & Mrs. Nazia Parveen | Professor                 |
| 5     | Advanced Digital Design with Verilog HDL | EC-517                    | ADDVHDL         | ELECTIVE           | Mr. Md. Zakir Hussain                | Asst.Prof.                |
| 6     | Neural Network & Fuzzy Logic             | EC-578                    | NNFL            | ELECTIVE           | Dr.Mrs. Arshia Azam                  | Asso. Prof.               |
| 7     | Digtal Systems Lab-II                    | EC-507-2                  | DS-LAB-II       | CORE               | Mr.Md.Zakir Hussain/Mr. M.A. Raheem  | <b>As mentioned above</b> |
| 8     | Seminar-II                               | EC-508-2                  | SEM-II          | CORE               | Dr. C. Rangaiah / Mrs. Ayesha Naaz   |                           |

***Revised***