

GENERAL INFORMATION AND ACADEMIC BACKGROUND

1. Name (in Block Letters) : MALIHA NAAZ
2. Place of Work : HYDERABAD
3. Department & College : ECE & MJCET
4. Current Designation : Assistant Professor
5. Date and Place of Birth : 17th April, Hyderabad
6. Gender : Female
7. Nationality : India
8. Address for Correspondence (with Pin Code) : 9-4-131/90,
Tolichowki,
Hyderabad,INDIA.
PIN: 500008.
9. Mobile No : +91- 9885443688

Email: maliha.naaz@gmail.com

10. Academic Qualifications (copies of certificates to be enclosed)

Name of the Degree	Specialization	Awarding University	Division	Year Passed
M.E	Digital Systems	Osmania, Hyderabad	FirstClass with distinction	2012
B.E	Electronics and Communication	Osmania, Hyderabad	First Class with distinction	2009

XII th class	Mathematics, Physics, Biology, Chemistry	Board of Intermediate	First Class with distinction	2005
X th class	Mathematics, Social Studies, Science	Board of Secondary Education	First Class with distinction	2003

11. Appointment held prior to joining Osmania University service: [Enclose copies of
a) Appointment Orders b) Relieving Orders c) Proceedings of Selection Committee
d) Service Certificate, etc.]

Name of the Institution with address	Post held in Teaching/Industry	Duration From ---To	Total Years
MJCET, Hyderabad	Assistant Professor	29-11-2012- till date	11 years
MJCET, Hyderabad	Part-Time Teaching Assistant	July 2012-Oct 2012	4 months
MJCET, Hyderabad	Academic Consultant	Mar 2012-July 2012	5 months
Total Experience			11 years 9 months

12. Teaching experience:

U.G level & P.G. level : 11 years

13. Research experience excluding years spent pursuing M.Phil/ Ph.D : 04 Years

14. Fields of Specification under the Subject / Discipline

- Analog IC Design
- Analog and Mixed Signal Design
- Power Management circuits
- VLSI design

15. Orientation / Refresher Courses attended at Academic staff College:

**Refer Serial No: V of this document

Related Details:

Subjects Taught	Electronic Devices Theory & Lab, Analog Electronic Circuits Theory & Lab, Analog IC Design Theory, Basic Electronics Theory & Lab, Electronic Engineering-II Theory & Lab, Basic Circuit Analysis Theory, VLSI design
List of publications	Refer Serial No I, II, III,IV
FDP/Workshops/Conferences/Seminars/ Extra curricular	V

I. List of publications – Journals

[1] Design of low power notch filter for biomedical applications.
IJIREEICE. 2016 Dec;4(12):75-9.

II. List of Conferences

2023	M. Naaz, K. Fatima, B. R. Naik, S. A. A. Sayeed, S. S. Ahmed and M. S. Ashher, "Design of DC-DC Boost Converter Using PFM Switching Technique," 2023 IEEE Devices for Integrated Circuit (DevIC), Kalyani, India, 2023, pp. 368-371, doi: 10.1109/DevIC57758.2023.10134869.
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2023	"A DC-DC Boost Converter using PWM with 65%efficiency," 2023 IEEE Devices for Integrated Circuit (DevIC), Kalyani, India, 2023, pp. 377-382, doi: 10.1109/DevIC57758.2023.10134808.
2022	IEEE- WINTECHCON 2022, IEEE CAS Bangalore Chapter, June 02-03, 2022 Title: Wearable Devices and Body Area Networking (Invited Tutorial)
2022	"A Survey Paper on Capacitor Less Low Dropout Regulator," 2022 International Conference on Electrical, Computer and Energy Technologies (ICECET), Prague, Czech Republic, 2022, pp. 1-6, doi: 10.1109/ICECET55527.2022.9872998.
2019	"A 1.8V 204.8 μ W 12-Bit Fourth Order Active Passive $\Sigma\Delta$ Modulator for Biomedical Applications," 2019 Devices for Integrated Circuit (DevIC), Kalyani, India, 2019, pp. 124-127, doi: 10.1109/DEVIC.2019.8783626.
2017	Mohammed Arifuddin Sohel, Maliha Naaz, M.A. Raheem, M.A. Munaf "Design of discrete time notch filter for biomedical applications" IEEE Dev IC 2017, Kalyani Engineering College, Kolkatta, March 2017
2015	"A Logarithmic DWA based Discrete Time Multi bit Sigma Delta Modulator" IEEE IMBUSH ERA, Amity University, February 2015.
2015	Design of switched capacitor based Notch filter", National Conference on Circuits Signals and Systems(NCCSS), Organised by MJCET, Hyderabad, Year: 2015, Pages:31-35
2013	IEEE PrimeAsia 2013, Vizag, December 2013, Page(s): 1 – 6 Title: A Low Power Continuous Time Band Pass Sigma Delta Modulator using linearity enhanced OTA
2011	" A 2.5V high swing Two stage CMOS Operational Amplifier in 0.25 Micron Technology" is accepted for publication in National Conference on Advances in Communication and Electrical Engineering NCACEE11 at Regency College of Engineering, Yanam, Puducherry held on 11 th November 2011.

IV. Workshops attended

Year	Workshop/FDP details	Date
2023	One week National level capacity building programme on " cybersecurity and Forensics" By KJ somaiyainstitute of technology , Sion and Ramrao Adik Institute of Technology , Nerul	19th June to 24th June 2023
2022	IEEE sponsored 6 days FDP on Awareness of Electronic tools for system design from.	19th to 24th December 2022
2022	vlsi revisted, 5 weeks online, online employability enhancement summer school on VLSI,	21st June-25 June 2022

2021	ATAL FDP on "Custom IC Design in the Advanced CMOS Technology", at Indian Institute of Information Technology Pune.	21/11/2021 to 25/11/2021
2021	ATAL FDP on "Research Issues in VLSI Design and Testing" from at Government College of Engineering Srirangam.	02-08-2021 to 06-08-2021
2021	“Memory Design Workshop” organized by IEEE Circuits and Systems (CAS) Society Bangalore Chapter in association with IEEE Bangalore Section	14th August 2021 to 30th August 2021.
2021	Two Week Faculty Development Program on “Full – Custom Design of an IC using Mentor Graphics and Synopsys” organized by the Department of Electronics and Communication Engineering, Maturi Venkata Subba Rao (MVSR) Engineering College, Hyderabad in association with SOCDV Technologies Pvt. Ltd., Bengaluru	09 – 21, August 2021.
2021	Three-Day Faculty Development Program on “Mixed Signal VLSI Circuit Design” Conducted by Anurag Group of Institutions(AGI),Hyderabad under Equity Action Plan, TEQIP-III JNTUH, Hyderabad	25th to 27th January 2021
2021	FDP on "Custom IC Design in the Advanced CMOS Technology" at Indian Institute of Information Technology Pune.	21/11/2021 to 25/11/2021
2021	Online Elementary FDP on " Research Issues in VLSI Design and Testing" at Government College of Engineering, srirangam.	2021-08-02 to 2021-08-06
2021	Five Day Online Faculty Development Program under the MOU with AICTE on “Emerging Trends in RF and Energy Device and Circuits” organized by the Department of Electronics and Communication Engineering at NIT Meghalaya	22nd Feb to 26th Feb, 2021.
2020	One week online FDP on Recent Advances in Communications & Signal Processing organized by JNTU kakinada.	24.8.2020 to 29.08.2020
2020	Five days online Faculty Development Program on “Advances in 5G-Wireless Communications with Hand on Experiences” organised by Department of Electronics and Communication Engineering, Lendi Institute of Engineering and Technology	16th- 20th, June 2020.
2020	Seasonal School 2020 - Technology and Agribusiness How Technology is Impacting Agribusiness held on, IEEE-CAS & Universidad Santo Tomas	November 16 – 20, 2020
2020	IEEE Workshop on “Microwave and Antenna Applications in 5G” ,jointly Organized by AP-MTT	8th-13thDec 2020

	Chapter Bangalore and IEEE Bangalore Section	
2019	AICTE/FDP on Fundamentals of Electronic Device Fabrication	29th July-29th Sept 2019
2019	3-Day Hands on Workshop on " Setup of Internet of Things(IoT) Lab"	25-27 March 2019
2018	Two Day Industry Workshop on "Analog IC Design – Concept to Reality"	31st August- 1st September 2018
2017	NPTEL Online Course : Analog Circuits and Systems through SPICE Simulation	24th Jul-22 Oct 2017
2017	Short Term Course on Engineering Research Methodology	11-14 December, 2017
2017	Faculty Development Program cum Short Term Course on Analog IC Design(AICD)- Circuit and Layout Design Methodologies Using Cadence Design Flow	11-14 August, 2017
2017	Two-Week ISTE STTP on CMOS, Mixed Signal and Radio Frequency VLSI Design	30th Jan 2017 -4th Feb 2017
2016	2 Day Faculty Development Program on " Building Teacher Effectiveness"	21-22 October 2016
2016	IEEE CAS/EDS 1 Day Ph.D Forum	16th July, 2016
2015	Two-Week ISTE STTP on Technical Communication	8th October- 5th December 2015
2015	2 Day Orientation Programme on " The Art and Aesthetics of Teaching"	1-2 August, 2015
2014	Two Day Workshop on Analog IC Design- Spec to Reality	18-19 September 2014
2014	Two-Week ISTE STTP on Computer Networking	28th May- 5th July 2014
2014	Two Day Workshop on Micro Electro Mechanical System (MEMS) Design	20-21 June, 2014

V. Workshops conducted

1. Two day orientation workshop on Introduction to Electronics, Coordinator, from 09th -10th May 2016, Muffakham Jah College of Engineering and Technology.
2. National Conference on Circuits, Signals and Systems, Organizing Committee from 22nd-24th January 2015, Muffakham Jah College of Engineering and Technology.

VI. As speaker/Judge of student events

1. Session chair for ICECCME-2022 international conference held during November 16-18 2023.
2. Elocution competition on Engineers day, as Judge, on 14 September 2019, held at Muffakham Jah College of Engineering and Technology.

VII. Online Certifications

1. NPTEL Online Course on “Analog Circuits and Systems through SPICE Simulation” by IIT-Kharagpur, Jul-Oct 2017
2. NPTEL Online Course on “Fundamentals of electronic device fabrication”,by IIT Madras, Jun-Sep 2019

VIII. Extra Curricular Activities

1. Participated and achieved 1st Position in Chess in the Faculty Sports organized by Physical Department of Muffakham Jah College of Engineering & Technology in 2015 and 2016.

IX. Awards and Invited Lectures delivered in conferences

1. Invited tutorial in IEEE- WINTECHCON 2022, IEEE CAS Bangalore Chapter, June 02-03, 2022.Title: Wearable Devices and Body Area Networking
2. Received certificate for “Excellent” work for designing “Design and Simulation of 3 Stage Charge Pump using Synopsys Custom Compiler at 28nm Technology node” in Cloud based Analog IC Design Hackathon organized by IIT Hyderabad, which has been sponsored by Synopsys in association with VLSI System Design (VSD) Pvt., (2022).
3. One of the topper in the NPTEL Online Course on “Analog Circuits and Systems through SPICE Simulation” by IIT-Kharagpur.

X. Appreciations:

1. Received appreciation letter for achieving good feedback for teaching Analog

Electronic Circuits, Basic Circuit Analysis, Analog IC Design, Electronic Devices.

2. Received appreciation letter for working as Teaching Assistant in workshop on CMOS, Mixed Signal & Radio Frequency VLSI Design from IIT- Kharagpur.
3. Received appreciation letter for assisting in organizing Analog IC Design CAMP at Muffakham Jah College of Engineering & Technology.

XI. Academic Achievements:

1. Gold Medalist in M.E(Digital Systems) in year 2012

XII. Professional Memberships:

1. Member of IEEE – ID:93874622
2. Associate Member of IETE- ID:AM500741

Maliha Naaz