

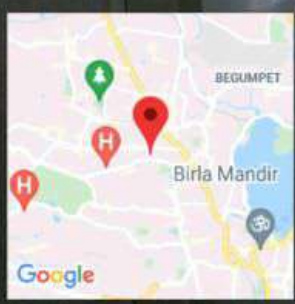
Room No
5708/A

CLASS ROOM
M.E. D.S. (ECED.)

Tutorial Room- PG



Hyderabad, Telangana, India



Longitude Latitude
78.4427° E 17.4283° N

Thursday, 16, Dec, 2021



25° C

02:37 PM

A handwritten signature in green ink, appearing to be 'M. J. M.', written in a cursive style.

PRINCIPAL
Muffakham Jah College Of
Engineering & Technology
Banjara Hills, Road No. 3,
HYDERABAD-500 034.(T.S.)



MUFFAKHAM JAH COLLEGE OF ENGINEERING & TECHNOLOGY
M.E / M TECH PROGRAMMES

CLASS TIME TABLE FOR M.E.DIGITAL SYSTEMS

[ELECTRONICS AND COMMUNICATION ENGINEERING DEPARTMENT]

1ST YEAR I-SEMESTER, 2019-2020

With Effect From-26-08-2019
BLOCK-5, Room No-5709 7th floor for Professional Core & Elective Subjects and-----
BLOCK-5 Room No 5102 Ground Floor for Mandatory,
Open Elective and Audit course Subjects- (RM & IPR, DM, IS and WE).

PERIODS-->	1	2	3	4	1:00 P.M. TO 01:45 P.M.	5	6	7	
SESSION-->	←-----MORNING----->					←-----EVENING----->			
TIMINGS-->	9:00 A.M. TO 10:00 A.M.	10:00 A.M. TO 11:00 A.M.	11:00 A.M. TO 12:00 Noon	12:00 Noon TO 01:00 P.M.	LUNCH	1:45 P.M. TO 2:45 P.M.	2:45 P.M. TO 3:45 P.M.	3:45 P.M. TO 4:15 P.M.	
DAYS	SUBJECTS								
Day-1 MONDAY	Digital System Design	Image & Video Processing	Microcontrollers for Embedded System	Wireless Mobile Communication		LUNCH	Waste to Energy/Industrial Safety (Ground Floor, Room No 5102)		-----
Day-2 TUESDAY	Wireless Mobile Communication	Microcontrollers for Embedded System Design	Image & Video Processing	Digital System Design			*****Seminar*****		-----
Day-3 WEDNESDAY	Embedded systems Lab		Digital System Design	Image & Video Processing			Microcontrollers for Embedded System Design	Wireless Mobile Communication	-----
Day-4 THURSDAY	Digital System Design	Microcontrollers for Embedded System Design	Waste to Energy/Industrial Safety (Ground Floor, Room No.5102)				Disaster Management(Ground Floor, Room No 5102)		-----
Day-5 FRIDAY	9:00 A.M. TO 9:55 A.M.	9:55 A.M. TO 10:50 A.M.	10:50 A.M. TO 11:45 A.M.	11:45 A.M. TO 12:40 P.M.	FRIDAY LUNCH 12:40 P.M. TO 2:00 P.M.		2:00 P.M. TO 3:00 P.M.	3:00 P.M. TO 4:00 P.M.	-----
Day-6 SATURDAY	2nd year student's Project Work				LUNCH 1:00 P.M TO 1:45 P.M.		2nd year student's Project Work		

ALLOCATION OF SUBJECTS

S.NO.	NAME OF THE SUBJECT	COURSE CODE	SUBJECT CODE	COURSE TYPE	NAME OF THE FACULTY	DESIGNATION
1	Micro Controllers for Embedded System Design	PC 3101 EV	MCES	Professional Core	Mrs. Afshan Kaleem	Sr. Asst. Professor
2	Digital System Design	PC 3102 DS	DSD	Professional Core	Mr Md Sabir Hussian	Asst. Professor
3	Wireless Mobile Communication	PE 3121 DS	WMC	Professional Elective	Dr Nazia Parveen	Professor
4	Image & Video Processing	PE 3225 ES	IVP	Professional Elective	Mrs B Sucharitha	Asst. Professor
5	Industrial Safety / Waste to Energy	OE9105ME/ OE9104EE	IS / WE	Open Elective	Mr Krishna Murthy Dora / Dr Namratha	Asst. Professor/ Professor
6	Disaster Management	AD 9002 CE	DM	Audit Course	Ms.Asra Fatima	Asst. Professor
7	Embedded systems Lab	PC 3151 DS	LAB-I	Laboratory	Mr Hakeem Aejaz Aslam	Asst. Professor
8	Seminar	PC3154 DS	Seminar	Practical/Laboratory	Dr. Nazia Parveen/ Mr Md Sabir Hussian	Professor / Asst. Professor

Co-ordinator
M.E. Digital Systems

Chief Co-ordinator, M.E. M.TECH. Programmes

Head ECE Dept.

Advisor cum Director.

Time Table 1st year I-Semester, 2019-2020



Muffakham Jah College of
Engineering and Technology



MUFFAKHAM JAH COLLEGE OF ENGINEERING & TECHNOLOGY

M.E / M.TECH. PROGRAMMES

CLASS TIME TABLE FOR M.E. DIGITAL SYSTEMS

[ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT]

1ST YEAR II-SEMESTER, 2019-2020

NOTE: GROUP-B -M.TECH. COMPUTER SCIENCE & ENGG. (CSED) + M.E. DIGITAL SYSTEMS (ECED) STUDENTS

With Effect From-03-02-2020

For Professional Core & Elective Subjects

Room No-5708/A 7th floor ----

For Mandatory and Audit Course Subjects PG Seminar

Hall Room No- 5711----

For Open Electives Subjects Respective Class Rooms

PERIODS →	1	2	3	4	1:00 P.M. TO 1:45 P.M.	5	6	7	
SESSION →	-----MORNING-----					-----EVENING-----			
TIMINGS →	9:00 A.M. TO 10:00 A.M.	10:00 A.M. TO 11:00 A.M.	11:00 A.M. TO 12:00 Noon	12:00 Noon TO 01:00 P.M.		1:45 P.M. TO 2:45 P.M.	2:45 P.M. TO 3:45 P.M.	3:45 P.M. TO 4:15 P.M.	
DAYS	SUBJECTS ↓								
Day-1 MONDAY	VLSI Design & Technology	Optical Fibre Communication Systems	Mini Project With Seminar			LUNCH	Digital Signal Processors	*****	*****
Day-2 TUESDAY	Research Methodology and IPR		DSP Lab				Constitution of India & Fundamental Rights	*****	
Day-3 WEDNESDAY	Research Methodology & IPR	VLSI Design & Technology		Digital Signal Processors	Optical Fibre Communication Systems		*****		
Day-4 THURSDAY	Mini Project With Seminar		VLSI Design & Technology	Digital Signal Processors	DSD Lab		*****		
Day-5 FRIDAY	9:00 A.M. TO 9:55 A.M.	9:55 A.M. TO 10:50 A.M.	10:50 A.M. TO 11:45 A.M.	11:45 A.M. TO 12:40 P.M.	FRIDAY LUNCH 12:40 P.M. TO 2:00 P.M.		2:00 P.M. TO 3:00 P.M.	3:00 P.M. TO 4:00 P.M.	*****
Day-6 SATURDAY	2nd year student's Project Work				LUNCH 1:00 P.M TO 1:45 P.M.		2nd year student's Project Work		

ALLOCATION OF SUBJECTS ↓

S.NO.	NAME OF THE SUBJECT	COURSE CODE	SUBJECT CODE	COURSE TYPE	NAME OF THE FACULTY	DESIGNATION
1	VLSI Design & Technology	PC3103DS	VLSI D&T	Professional Core	Mr. M A Raheem	Asst. Professor
2	Digital Signal Processors	PC3104DS	DSP	Professional Core	Mrs Nazeerunnisa	Asst. Professor
3	Optical Fibre Communication Systems	PE3321EV	OFCS	Professional Elective	Dr.Nazia Parveen	Professor
4	Research Methodology and IPR	MC5161ME	RMIPR	Mandatory	Dr. Ishrat M M	Professor
5	Constitution of India and Fundamental Rights	AD 9011 HS	CIFR	Audit Course-II	Mr.K.Jaswant Prasad	Part Time Teacher
6	Digital System Design Lab	PC3152DS	LAB-II	Professional / Laboratory Course	Mr Md Sabir Hussian	Asst. Professor
7	DSP Lab	PC3153DS	LAB-III		Mrs.Nazeerunnisa	Asst. Professor
8	Mini Project With Seminar	PC5155DS	MPS		Mr Z. Hussian / Mr HA Aslam / Ms. M Naaz / Dr. Nazia P	Asst. Professor

Co-

ordinator,
M.E.Digital Systems

Chief Co-ordinator, M.E. M.TECH. Programmes

Head ECE. Engg. Dept.

Chief Superintendent
Examination Cell

Advisor cum Director.

Time Table 1st year II-Semester, 2019-2020